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	Subclass
	Class
	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

<i>B</i>	O.I.P.E.	PATENT DATE
SCANNED 11/3	Q.A. CTH	

APPLICATION NO. 09/671065	CONT/PRIOR	CLASS 711710	SUBCLASS 129	ART UNIT 2185 2181	EXAMINER VO
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APPLICANTS
Michael Perez

TITLE
Mechanism for allowing PCI-PCI bridges to cache data without any coherency side effects

PTO-2040

2008
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ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			ISSUE BATCH NUMBER	

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